



**AMENDMENT UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE-EXAMINING
GROUP 2822**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of :
Miwa WAKE et al. :
Serial No. 10/766,587 : Group Art Unit: 2822
Filed: January 28, 2004 : Examiner: Thanh Y. Tran
For: METHOD OF MANUFACTURING :
A SEMICONDUCTOR :
INTEGRATED CIRCUIT AND :
SEMICONDUCTOR INTEGRATED :
CIRCUIT : Docket No. S004-4839 (DIV)

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MS AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**AMENDMENT AFTER FINAL
UNDER RULE 116**

S I R:

In response to the Office Action mailed October 10, 2006, applicants amend their application as follows:

ADDITIONAL FEE:

No additional fee is believed required in connection with this amendment. However, should it be determined that a fee is due, authorization is hereby given to charge any such fee to our Deposit Account No. 01-0268.

BY MAILING CERTIFICATE ON PAGE 6